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PATENT  
1248-0497P  
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IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: SHIGEMASA et al. Conf.: 3009  
Appl. No.: 09/552,701 Group: 2184  
Filed: April 19, 2000 Examiner: E. C. PUENTE  
For: MICROCOMPUTER HAVING BUILT-IN  
NONVOLATILE MEMORY AND CHECK SYSTEM  
THEREOF AND IC CARD PACKING  
MICROCOMPUTER HAVING BUILT-IN  
NONVOLATILE MEMORY AND CHECK SYSTEM  
THEREOF

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**AMENDMENT UNDER 37 C.F.R. § 1.111**

Technology Center 2100

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

January 20, 2004

Sir:

In response to the Office Action dated October 20, 2003, the following amendments and remarks are respectfully submitted in connection with the above-identified application as follows:

Amendments to the claims are reflected in the listing of claims which begins on page 2 of this paper

Remarks/Arguments begin on page 11 of this paper.